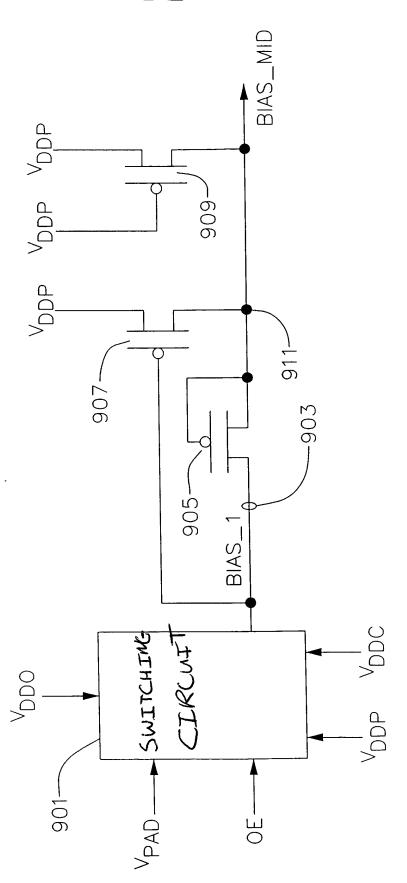


FIG.9A

Application No. 10/043,763
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS
Sheet 9 of 23

Sheet <u>9</u> of <u>23</u>

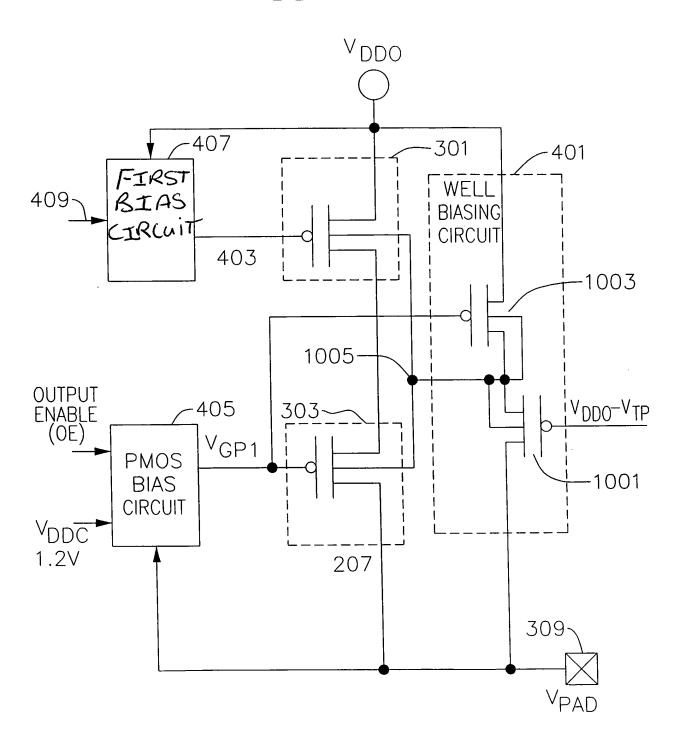




Application No. 10/043,763
inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

Sheet <u>11</u> of <u>23</u>

## FIG. 10

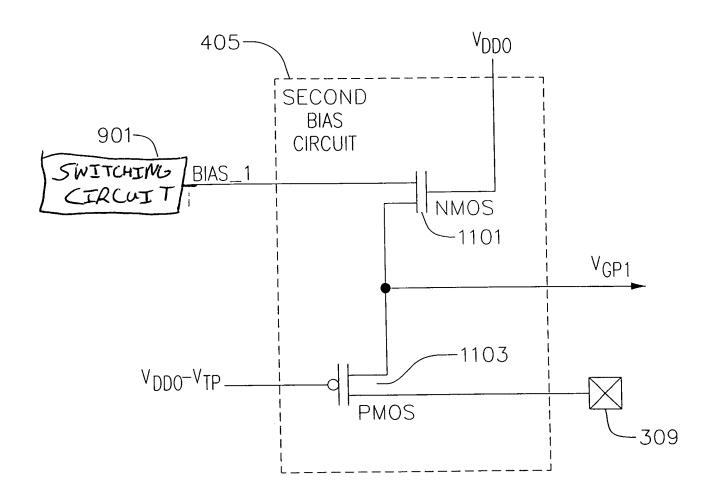




Application No. 10/043,763
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT V. GE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

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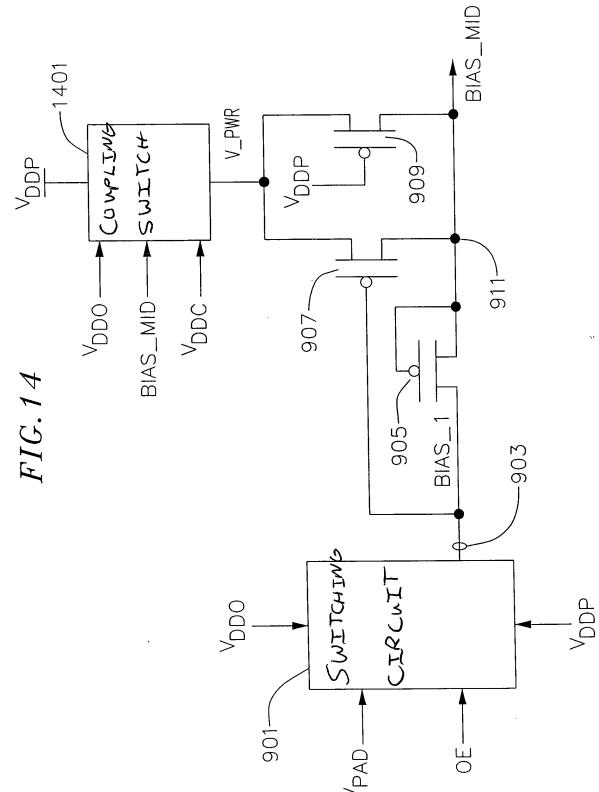
## FIG. 11A





Application No. 10/043,763 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS

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Application No. 10/043,763 howentor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

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## FIG. 18

